

FIGURE 1

The diagram illustrates a computer system architecture. It features a top address bus labeled "ADDRESS:" connected to a series of memory locations represented by diamond symbols. The central component is the "TEST COMPUTER CONFIGURATION" block, which contains three primary modules: "COMPUTER MODEL #" (addressed by ~22), "RAM" (addressed by ~23), and "EXTERNAL DEVICES" (addressed by ~24). To the right of this main configuration block, there is a vertical column of four additional components, each associated with a specific address label (~20, ~21, ~22, and ~23).

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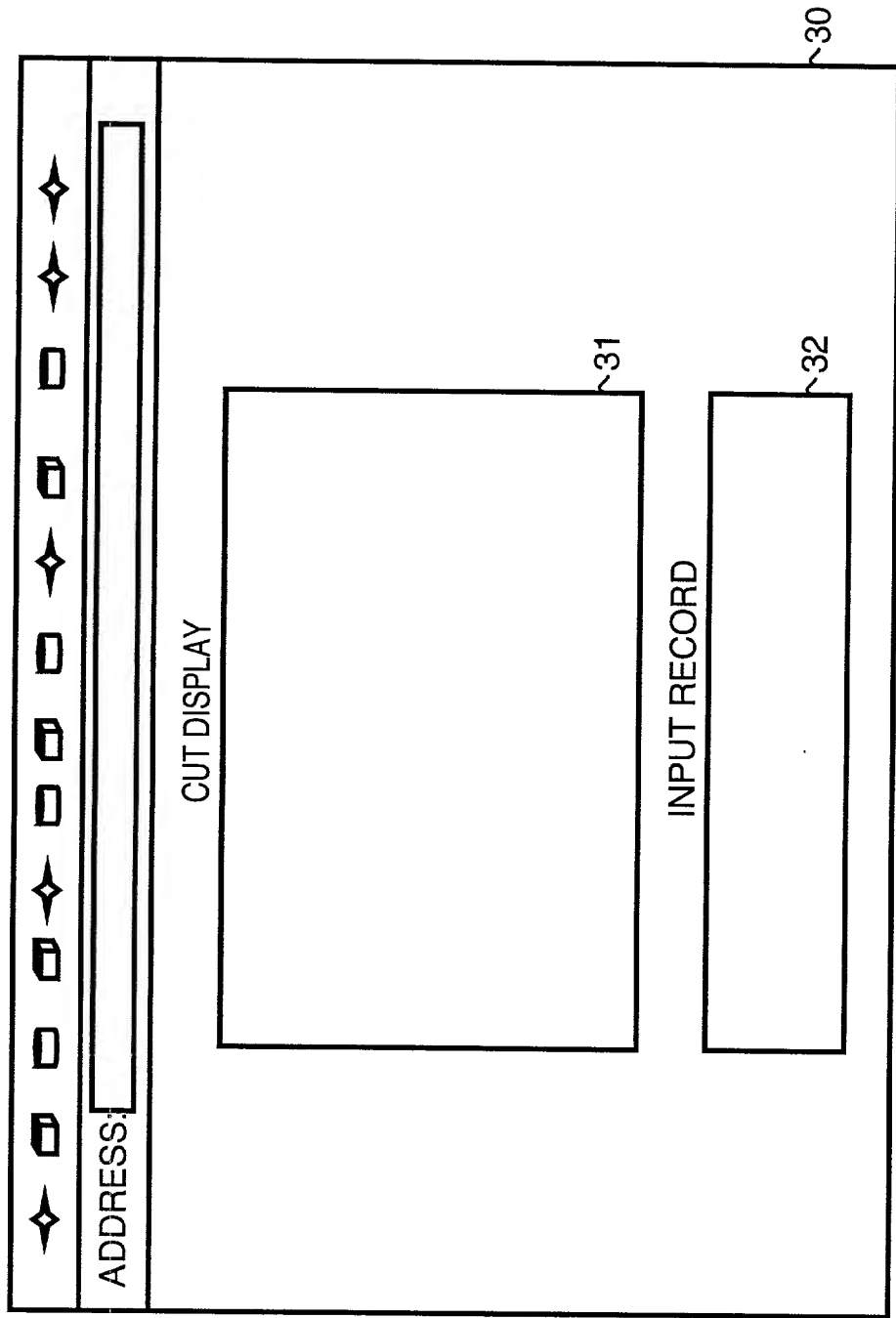


FIGURE 3

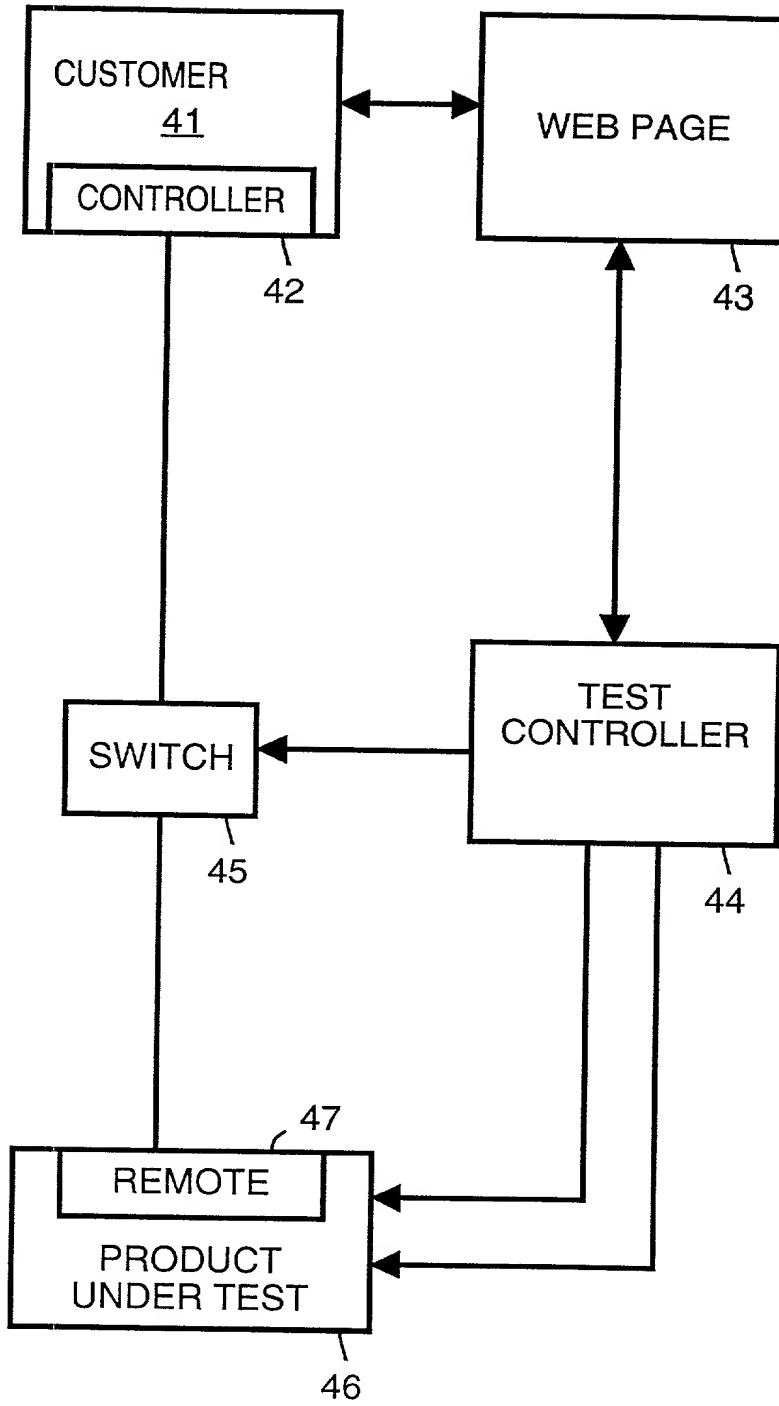


FIGURE 4

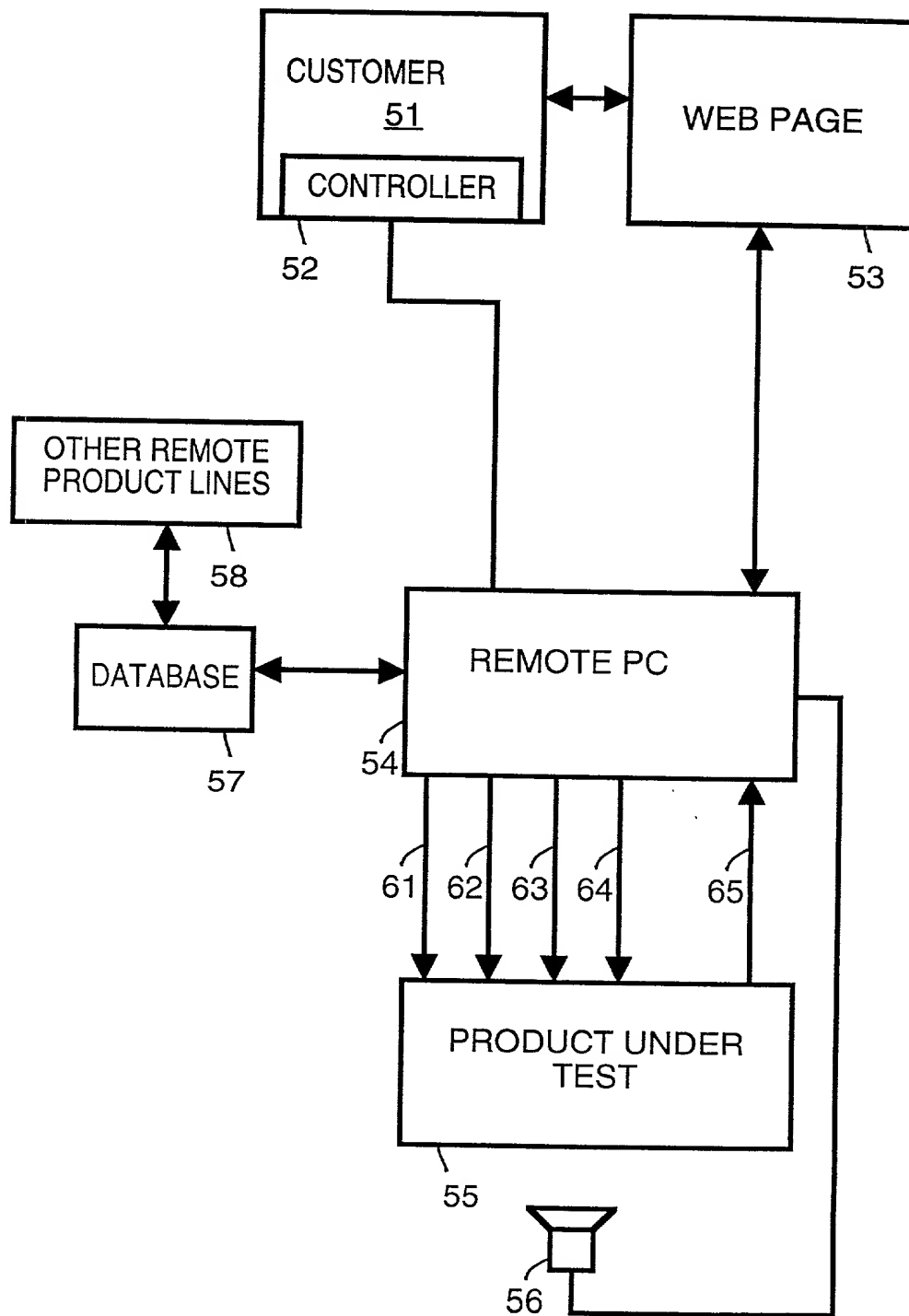


FIGURE 5

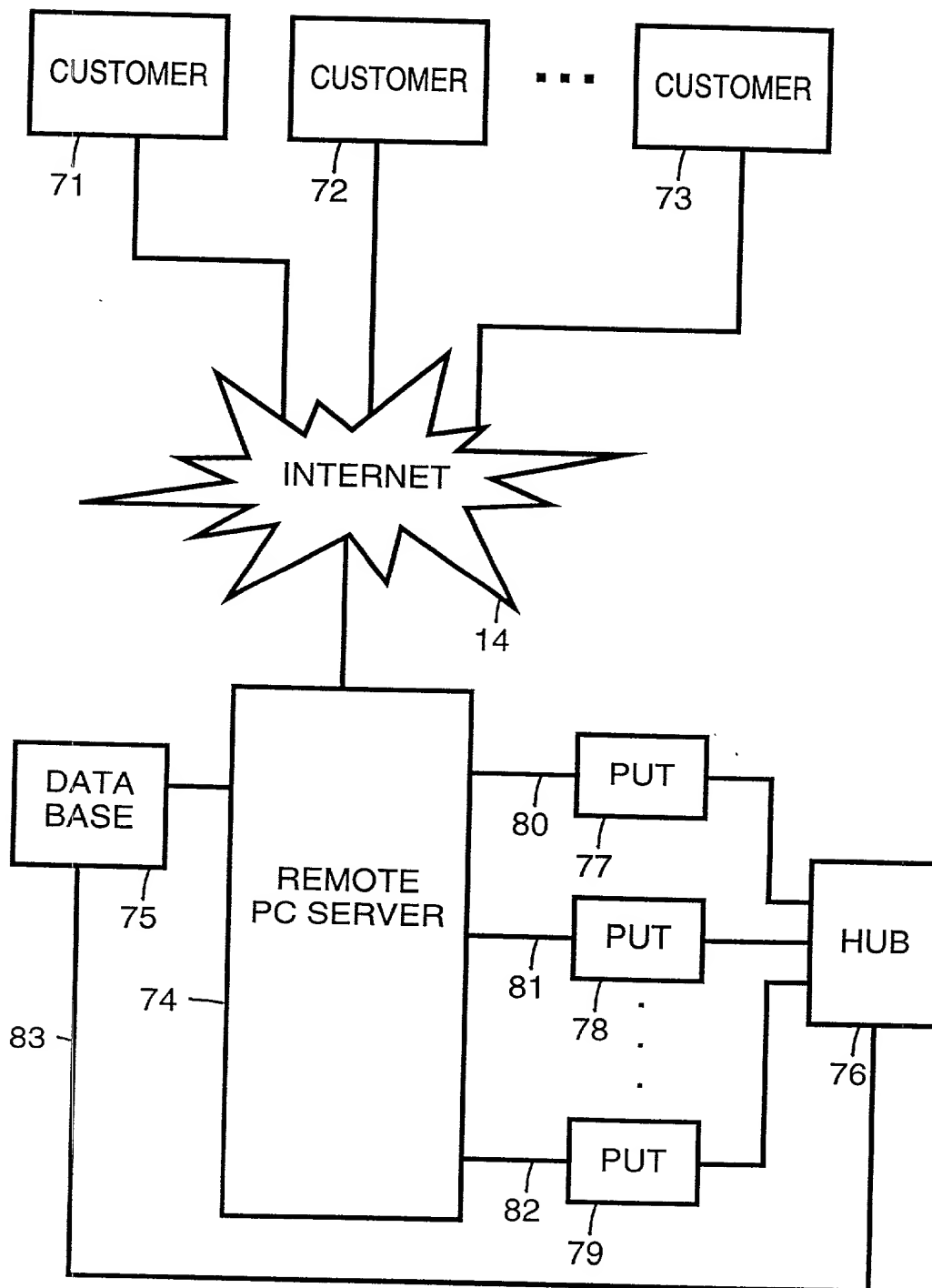


FIGURE 6

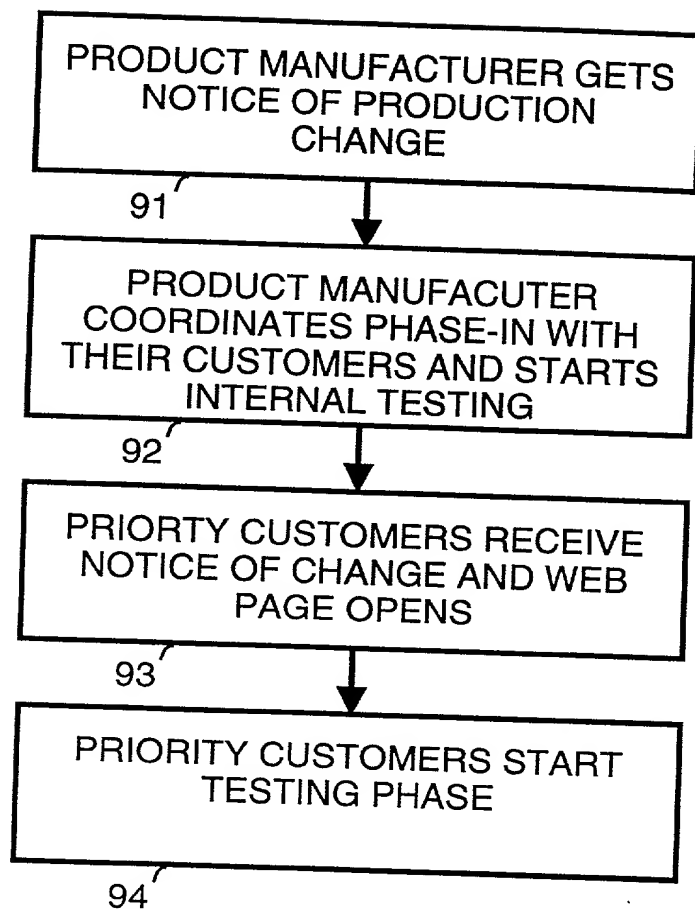


FIGURE 7